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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.: 09/239,194

Confirmation No.: 9716

Applicant: John S. Yates, Jr., et al.

Title: EXECUTING PROGRAMS OF A FIRST COMPUTER ARCHITECTURE
ON A COMPUTER OF A SECOND ARCHITECTURE

Filed: January 28, 1999

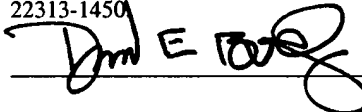
Art Unit: 2127

Examiner: Kenneth Tang

Atty. Docket: 114596-05-4013

Customer No. 38492

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AFTER FINAL – EXPEDITED PROCEDURE

RESPONSE TO OFFICE ACTION

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicant hereby responds to the Office Action of January 13, 2005.

PROPOSED AMENDMENTS TO THE CLAIMS are set forth in the Appendix to this paper.

REMARKS/ARGUMENTS begin on page 1 of this paper.

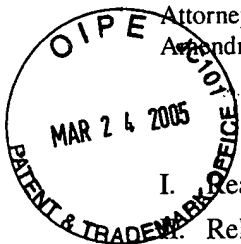


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Exhibit 1 IBM System 370 Principles of Operation, IBM Corp., pages (1981)

Exhibit 2 Intel Architecture Software Developer's Manual, vol. 3, System Programming Guide (1997)

Exhibit 3 Wikipedia definition of “thread” (http://en.wikipedia.org/wiki/Thread_%28computer_science%29)

Exhibit 4 Description of setjmp and longjmp from <http://www.hmug.org/man/3/setjmp.html>